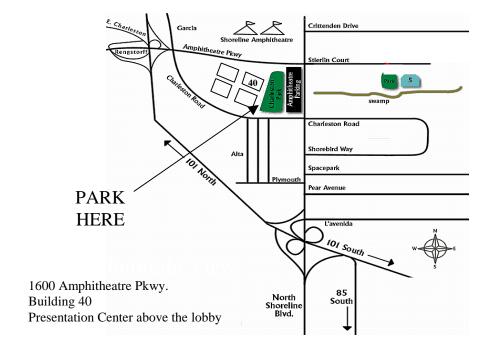
SCV-CHAPTER OF THE IEEE EMC SOCIETY MEETING LOCATION



MEETING TIME: Social – 5:30 PM Presentation – 7:00 PM

Dated Material - Meeting Notice Address Service Requested **Spectral Lines**

IEEE SCV EMC Society c/o N. Pischl P.O.Box 4570 Mountain View, CA 94040-5070







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WWW.SCVemc.org Santa Clara Valley Chapter of IEEE Electromagnetic Compatibility Society

October 2001 issue

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$$\nabla \times \overrightarrow{E} = -\frac{\partial \overrightarrow{B}}{\partial t}$$

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$$\nabla \cdot \overrightarrow{\mathbf{D}} = \mathbf{p}$$

$$\nabla \cdot \overrightarrow{\mathbf{B}} = \mathbf{0}$$

SCV EMC Society Meeting Tuesday, October 9, 2001 Doug Smith: EFT Testing and Common Pitfalls

The October meeting of the Santa Clara Valley EMC Society will be held at SGI in Mountain View, 1600 Amphitheatre Pkwy., building 40, in the Presentation Center above the lobby. The social gathering will start at 5:30 PM, and the presentation starts at 7:00 PM. A map with the location of the SGI campus is available in this newsletter.

Doug Smith, will cover the background of EFT testing (IEC 61000-4-4) walong with common pitfalls. Doug will also share some tips on the testing process. Doug believes that all talks should have some entertainment value and this talk is not an exception. War stories and demonstrations will be used throughout the talk.

One of these covers a common mistake that some testing labs make that can nearly double the stress on the EUT. If you have an EFT Burst Generator what do you do with it when it is not being used (probably most of the time unless you work for a test lab)? Doug will cover some test / debug techniques using an EFT generator that have nothing to do with EFT, such as solving ESD problems and measuring noise margins at the PWB and circuit trace level.

About the presenter:

Mr. Smith received a B.S.E.E. degree from Vanderbilt University in 1969 and an M.S.E.E. degree from the California Institute of Technology in 1970. In 1970, he joined AT&T Bell Laboratories as a Member of Technical Staff. He retired in 1996 as a Distinguished Member of Technical Staff. From February 1996 to April 2000 he was Manager of EMC Development and Test at Auspex Systems in Santa Clara, CA. Mr. Smith currently is an independent consultant specializing in high frequency measurements, circuit/system design and verification, switching power supply noise and specifications, EMC, and immunity to transient noise. He is a Senior Member of the IEEE and a member of the IEEE EMC Society Board of Directors.

His technical interests include high frequency effects in electronic circuits. He has been granted over 15 patents, several on measurement apparatus. Mr. Smith has lectured at Vanderbilt University, AT&T Bell Labs, and at many public and private seminars on high frequency measurements, circuit design, ESD, and EMC. He is author of the book High Frequency Measurements and Noise in Electronic Circuits and is currently working on his second book.

Santa Clara Valley Chapter of IEEE EMC Society is currently seeking nominees for the Secretary and Treasurer. If you would like to nominate someone, or if you would like to volunteer, please let us know at the meeting, or send a mail to the Secretary, npischl@terawave.com. Also, if you have received Spectral Lines vie paper-mail, please send your e-mail address to the Secretary. Thank you.

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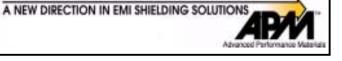
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