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IEEE/EMC/SCV CHAPTER Meeting Tuesday May 13, 2003

LOCATION: Applied Materials Bowers Café 3090 Bowers, Santa Clara, CA 95051-0804 5:30 p.m. Social 7:00 p.m. Presentation







Spectral Lines

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May 2003 Issue

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SCV IEEE/EMC Society Meeting Tuesday, May 13, 2003

Time: 5:30 p.m. Social, 7:00 p.m. Intro, 7:15 p.m. Presentation

Place: Applied Materials Bowers Cafe, 3090 Bowers Avenue,

Santa Clara, CA 95051-0804

Subject: "Considerations for Advanced EMC Design Flow"

Speaker: Sudip Das, Senior EMC Engineer, Cisco Systems, Inc.

Abstract:

In an ideal world, every hardware project should have an engineer responsible for functional design, regulatory compliance, signal integrity (SI), CAD, and manufacturing. Each of them would get ample time to thoroughly check and simulate their portions of the design. But in the real world, we are often faced with a lack of both time and resources. What can be done to make our design process effective, even in very fast-paced projects, so that none of the design objectives are overlooked? Another challenge would be: how to introduce this new process without breaking down the existing legacy process? And, of course, how and where does EMC fit in this overall process flow? One approach would be to embed the design requirements in the CAD tools, and by efficiently designing the schematics and PCB layout to preserve the compliance and SI knowledge base from one project to another.

Biography:

Sudip Das is an EMC engineer at Cisco systems. He has been working as an EMC engineer for the last eight years. Before joining Cisco Systems, he was previously employed at Sun Microsystems. His primary focus areas are EMC, CAD design and SI, and overall hardware design process flow. He has worked with various types of products starting from high-end super computers to low-end ultra-cost sensitive home office products using offshore companies. He has authored several papers in IEEE EMC journals. He received his Ph. D and MS degree in Electrical Engineering from University of Kentucky and B.Tech in Electronics from IIT Kharagpur, India.

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