

# Spectral Lines

www.scvemc.org Santa Clara Valley Chapter of IEEE Electromagnetic Compatibility Society

February 2003 Issue

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## SCV IEEE/EMC Society Meeting Tuesday, February 11, 2003

Date: February 11, 2003 (second Tuesday of the month)

Society Chapter: IEEE EMC Society

Subject: Vcc and Ground Bounce in planes and IC packages.

Speaker: Lee Ritchey

Time: 5:30 Social, 7:00 Presentation

Place: Applied Materials Bowers Café, 3090 Bowers, Santa Clara, CA 95051-0804  
(This is a new meeting location, please check the IEEE web site (<http://www.scvemc.org/>) for directions.) RSVP is not required.

## Abstract:

Mr. Ritchey will be speaking on the topic of Vcc and Ground Bounce in planes and IC packages and how it may be the cause of radiated and conducted emission problems from the enclosure and cables of your product. He will show us how using Hyperlynx will help to see what the emissions spectra looks like for these kinds of problems and what you may choose to do about it. He will also be sharing some of the most popular rules of thumb that we often hear about from our peers, such as right angle bends, vias, bypass capacitors, etc.

Mr. Ritchey will be using a combination of Powerpoint and the application used in hardware design known as Hyperlynx.

## Biography of Lee W. Ritchey

**LEE RITCHEY**, BSEE, is owner of Speeding Edge, a consulting firm specializing in high-speed design consulting and training. Prior to forming Speeding Edge, Mr. Ritchey spent five years as Director of Electronic Packaging Engineering at 3Com Corporation, a major manufacturer of high performance networking equipment. For ten years prior to that, he was a cofounder and Vice President of Engineering at Shared Resources, a company that specialized in the design of high performance printed circuit boards for the supercomputing industry. He has more than 30 years experience in the packaging of high performance equipment ranging from microwave satellites to super computers. He is currently working with major suppliers of gigabit and beyond internet products as well as a variety of wireless products. Mr. Ritchey has been on the editorial review board for Printed Circuit Design Magazine and is a regular contributor of design articles to a variety of publications. He has taught his High Speed Design course to more than 3000 engineers and designers in several countries. He is a regular lecturer at the Printed Circuit Design Conference, the IPC conferences and at UC Berkeley Engineering Extension.

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$$\nabla \times \vec{H} = \frac{\partial \vec{D}}{\partial t} + \vec{J}$$

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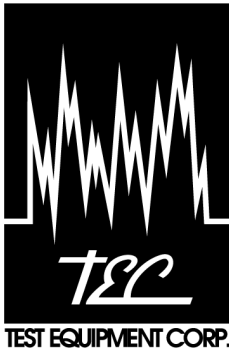


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